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(71) Applicant (for all designated States except US): **IMBERA ELECTRONICS OY** [FI/FI]; P.O. Box 74, FI-02151 Espoo (FI).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **IIHOLA, Antti** [FI/FI]; Koskelantie 33 A 5, FI-00610 Helsinki (FI). **JOKELA, Timo** [FI/FI]; Luhtimäentie 2b As. 1, FI-24800 Halikko (FI).

(74) Agent: **SEPPO LAINE OY**; Itämerenkatu 3 B, FI-00180 Helsinki (FI).

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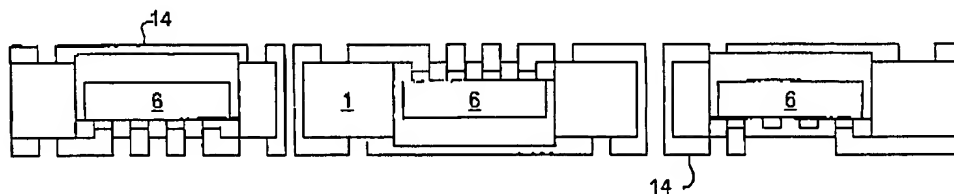
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(54) Title: **METHOD FOR MANUFACTURING AN ELECTRONIC MODULE**



(57) Abstract: This publication discloses a method for manufacturing an electronic module, in which manufacture commences from an insulating-material sheet (1). At least one recess (2) is made in the sheet (1) and extends through the insulating-material layer (1) as far as the conductive layer on the opposite surface (1 a). A component (6) is set in the recess, with its contact surface towards the conductive layer and the component (6) is attached to the conductive layer. After this, a conductive pattern (14) is formed from the conductive pattern closing the recess, which is electrically connected from at least some of the contact areas or contact protrusions of the component (6) set in the recess.

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METHOD FOR MANUFACTURING AN ELECTRONIC MODULE

The present invention relates to a method for manufacturing an electronic module.

5 In particular, the invention relates to an electronic module, in which one or more components are embedded in an installation base. The electronic module being manufactured can be a module like a circuit board, which includes several components, which are connected to each other electrically, through conducting structures manufactured in the module. The invention relates particularly to an electronic module,
10 which contains microcircuits, to which several contact terminals are connected. In addition to, or in place of microcircuits, other components too, for example, passive components, can, of course, be embedded in the installation base. Thus, the intention is to embed in the electronic module such components as are typically attached in an unpackaged form to the circuit board (to the surface of the circuit board). Another
15 important group of components are components that are typically packaged for connection to a circuit board. The electronic modules to which the invention relates can, of course, also include other types of components.

The installation base can be of a type similar to the bases that are generally used in the
20 electronics industry as installation bases for electrical components. The task of the base is to provide components with a mechanical attachment base and the necessary electrical connections to both components that are on the base and those that are outside the base. The installation base can be a circuit board, in which case the construction and method to which the invention relates are closely related to the manufacturing technology of circuit
25 boards. The installation base may also be some other base, for example, a base used in the packaging of a component or components, or a base for an entire functional module.

The manufacturing techniques used for circuit boards differ from those used for microcircuits in, among other things, the fact that the installation base in microcircuit
30 manufacturing techniques, i.e. the substrate, is of a semiconductor material, whereas the base material of an installation base for circuit boards is some form of insulating material. The manufacturing techniques for microcircuits are also typically considerably more expensive than the manufacturing techniques for circuit boards.

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The constructions and manufacturing techniques for the cases and packages of components, and particularly semiconductor components differ from the construction and manufacture of circuit boards, in that component packaging is primarily intended to form
5 a casing around the component, which will protect the component mechanically and facilitate the handling of the component. On the surface of the component, there are connector parts, typically protrusions, which allow the packaged component to be easily set in the correct position on the circuit board and the desired connections to be made to it. In addition, inside the component case, there are conductors, which connect the
10 connector parts outside the case to connection zones on the surface of the actual component, and through which the component can be connected as desired to its surroundings.

However, component cases manufactured using conventional technology demand a
15 considerable amount of space. As electronic devices have grown smaller, there has been a trend to eliminate component cases, which take up space, are not essential, and create unnecessary costs. Various constructions and methods have been developed to solve this problem, with the aid of which components can be placed inside the circuit-board structure.

20 US patent publication 4 246 595 discloses one solution, in which recesses are formed in the installation base for the components. The bottoms of the recesses are bordered by a two-layered insulation layer, in which holes are made for the connections of the component. The layer of the insulation layer that lies against the components is made of an adhesive. After this, the components are embedded in the recesses with their
25 connection zones facing the bottom of the recess, electrical contacts being formed to the components through the holes in the insulation layer. If it is wished to make the structure mechanically durable, the component must also be attached to an installation base, so that the method is quite complicated. It is extremely difficult to use a complicated method, which demands several different materials and process stages, to profitably
30 manufacture cheap products.

JP application publication 2001-53 447 discloses a second solution, in which a recess is made for the component in an installation base. The component is placed in the recess,

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with the component's contact areas facing towards the surface of the installation base. Next, an insulation layer is made on the surface of the installation base and over the component. Contact openings for the component are made in the insulation layer and electrical contacts are made to the component, through the contact openings. In this
5 method, considerable accuracy is demanded in manufacturing the recess and setting the component in the recess, so that the component will be correctly positioned, to ensure the success of the feed-throughs, relative to the width and thickness of the installation board.

International patent application publication WO 03/065778 discloses a method, in which
10 at least one conductive pattern is made in the base, as are through-holes for semiconductor components. After this, the semiconductor components are placed in the holes, aligned relatively to the conductive pattern. The semiconductor components are attached to the structure of the base and one or more conductive-pattern layers are manufactured in the base, in such a way that at least one conductive pattern forms an
15 electrical contact with the contact areas on the surface of the semiconductor component.

International patent application publication WO 03/065779 discloses a method, in which through-holes are made in the base for semiconductor components, in such a way that the holes extend between the first and second surfaces of the base. After the manufacture of
20 the holes, a polymer film is spread over the second surface of the base structure, in such a way that the polymer film also covers the through-holes made for the semiconductor components, on the second side of the base structure. Before the hardening of the polymer film, or after its partial hardening, the semiconductor components are placed in the holes made in the base, from the direction of the first surface of the base. The
25 semiconductor components are pressed against the polymer film so that they adhere to the polymer film. After this, the final hardening of the polymer film is performed and additional conductive-pattern layers are manufactured, in such a way that at least one conductive pattern forms an electrical contact with the contact areas on the surface of the semiconductor components.

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The invention is intended to create a simple and reliable method with low manufacturing costs, for embedding components in an installation base.

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The invention is based on commencing manufacture from an insulating board, which is surfaced on at least one side with a conductive layer. After this, a recess is made in the insulation, which opens onto one surface of the board, but does not penetrate the conductive layer on the opposite surface of the board. A component is attached to the recess and electrical contacts are formed between the conductive layer and the contact areas, or contact protrusions of the component. After the attachment of the component, conductive patterns are formed from this conductive layer, which become part of the circuit-board structure, or other electronic module.

More specifically, the method according to the invention is characterized by what is stated in Claim 1.

Considerable advantages are gained with the aid of the invention. This because the invention can be used to design a simple and reliable method with low manufacturing costs, which can be used to manufacture electronic modules containing embedded components. The conductively surfaced insulating board used as the basic material is one of the basic raw materials of the circuit-board industry and boards of this kind are available cheaply and reliably. In the method, the use of the raw material is extremely efficient, as the conductively surfaced insulating board is exploited to manufacture the conductive patterns of the electronic module. Even circuits that are embedded inside the insulating board can be connected electrically to this conductive-pattern layer.

The invention has embodiments, according to which relatively few process stages are required in the manufacturing process. Embodiments with fewer processing stages correspondingly also need less process equipment and various manufacturing methods. With the aid of such embodiments, in many cases it is also possible to reduce the manufacturing costs, compared to more complicated processes.

The number of the conductive-pattern layers of the electronic module can also be selected according to the embodiment. There can be, for example, one or two conductive-pattern layers. In addition, additional conductive-pattern layers can also be manufactured on top of them, in the manner known in the circuit-board industry. There can thus be a total of, for example, three, four, or five conductive-pattern layers in a module. In the

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very simplest embodiments, there is only a single conductive-pattern layer, and, indeed, any conductive layer. In some embodiments, each of the conductive layers contained in an electronic module can be utilized to form conductive patterns.

5 There are also embodiments of the invention, in which conductive patterns can be manufactured at the locations of the components. This will increase the wiring capability of the structure, which, in turn, will permit the components to be placed closer together. The wiring capability can also be improved by placing some of the components 'upside-down', so that the active surfaces of components will face both surfaces of the board. In
10 the following, the invention is examined with the aid of examples and with reference to the accompanying drawings.

Figures 1 - 17 show a series of cross-sections of the manufacturing methods in one embodiment of the invention.

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Figures 18 - 27 show a series of cross-sections of manufacturing methods according to second embodiments.

Figures 28 and 29 show two intermediate stages of the manufacture of an electronic
20 module in manufacturing methods according to third embodiments.

Figures 30 and 31 show two intermediate stages of the manufacture of an electronic module in manufacturing methods according to fourth embodiments.

25 In the methods of the examples, manufacturing is started by manufacturing an installation base of insulating material, which has a conductive layer on at least one surface. Typically, a commercially available sheet 1 of insulating material, both surfaces 1a, 1b of which are surfaced with a conductive layer 4, is selected as the installation base. The insulating material 1 can be, for example, glass-fibre-reinforced epoxy (e.g., FR4).
30 The conductive material 4 is, for its part, usually copper.

The installation base is typically selected in such a way that the thickness of the insulating material layer 1 is greater than the thickness of the components 6 to be later

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attached to the base, although this is not essential. Recesses 2, the size of which is selected according to the size of the components 6 being installed, are manufactured by a suitable method in the insulating-material layer 1. The recesses 2 are manufactured in such a way that the conductive layer 4 on the surface of the insulating material layer 1 closes one or other end of the recess. This is achieved, for example, by removing the conductive material 4 on the first surface of the installation base around the recess 2. In connection with the removal of the conductive material 4, other patterns can also be formed in the conductive-pattern layer 4, for example, the patterns of the conductors of the circuit that will be formed. After this, the making of recesses 2 is continued using a suitable selective method, which affects the insulating material 1 but not the conductive layer 4. The recess 2 thus manufactured should extend through the entire insulating material layer 1, while the conductive layer 4 at the other end of the recess 2 remains undamaged. Recesses 2 can be made in a corresponding manner from the directions of both surfaces.

Suitable alignment marks are also required to align the components 6, for the creation of which several different methods are available. One possible method is the making of small through-holes in the vicinity of the installation holes 2 of the components 6.

The components 6 are aligned in their installation holes 2 with the aid of the alignment holes or other alignment marks and the components are attached to the conductive layer 4. The components 6 can be attached to the conductive layer 4 using a method that permits the formation of an electrical contact between the conductive layer 4 and the contact areas of the component. Such methods are, for example, the ultrasonic bonding method, the thermo-compression method, and gluing with an electrically conductive adhesive. Alternatively, it is possible to use a method, in which an electrical contact is formed between the conductive layer 4 and the contact areas of the component. Such a method, is, for example, gluing with an insulating adhesive. In the following, the procedure of the process is described in greater detail, in connection with the attachment methods referred to above.

The term ultrasonic methods refers to a method, in which two pieces containing metal are pressed together and vibration energy is brought to the attachment area at an ultrasound

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frequency. The ultrasound and the pressure created between the surfaces being attached cause the pieces being attached to bond metallurgically to each other. Methods and devices for creating ultrasound joints (ultrasonic bonding) are commercially available. Ultrasonic bonding has the advantage that a high temperature is not required to form the joint.

The term thermo-compression method refers to a method, in which two pieces containing metal are pressed against each other and thermal energy is applied to the joint area. The thermal energy and the pressure created between the surfaces being attached cause the pieces being attached to bond metallurgically to each other. Methods and devices for making thermo-compressed joints (thermo-compression bonding) are also commercially available.

The term adhesive refers to a material, by means of which the components can be attached to the conductive layer. One property of the adhesive is that the adhesive can be spread on the surface of the conductive layer, and/or of the component in a relatively fluid form, or otherwise in a form that will conform to the shape of the surface. Another property of the adhesive is that, after spreading, the adhesive hardens, or can be hardened, at least partly, so that the adhesive will be able to hold the component in place (relative to the conductive layer), at least until the component is secured to the structure in some other manner. A third property of the adhesive is its adhesive ability, i.e. its ability to stick to the surface being glued.

The term gluing refers to the attachment of the component and conductive layer to each other with the aid of an adhesive. Thus, in gluing, an adhesive is brought between the component and the conductive layer and the component is placed in a suitable position relative to the conductive layer, in which the adhesive is in contact with the component and the conductive layer and at least partly fills the space between the component and the conductive layer. After this, the adhesive is allowed (at least partly) to harden, or the adhesive is actively hardened (at least partly), so that the component sticks to the conductive layer with the aid of the adhesive. In some embodiments, the contact protrusions of the component may, during gluing, extend through the adhesive layer to make contact with the conductive layer.

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The components 6 can thus be attached to the surface of the conductive layer 4 with the aid of an electrically conductive adhesive. Electrically conductive adhesives suitable for this purpose are generally available in two basic types: isotropically conductive adhesives and anisotropically conductive adhesives. An isotropically conductive adhesive conducts in all directions, whereas an anisotropically conductive adhesive has a conductive direction and a direction diametrically opposite to this, in which the conductivity of the adhesive is extremely low. An anisotropically conductive adhesive can be formed, for example, from an isolating adhesive, into which suitable conductor particles are mixed. If an anisotropically conductive glue is used, the glue can be dosed over the component's entire surface that is being glued. When using an isotropically conductive glue, dosing should be performed by area, so that short-circuits are not created between the contact areas.

After the attachment of the components, the space remaining in the installation recess 2 is typically filled with a filler 8. After this, the conductive layer 4 can be patterned, so that conductive patterns 14 are formed, at least some of which are connected to the contact areas of some of the components 6. After this, the process can be continued by manufacturing additional conductive-pattern layers and manufacturing the necessary through-holes.

Manufacturing exploiting the ultrasonic method and the thermo-compression method are disclosed in greater detail in the same applicant's Finnish patent application FI20030292, filed on 26 February 2003, which is still confidential at the time of filing of the present application.

Manufacturing exploiting conductive glues are, in turn, disclosed in greater detail in the same applicant's Finnish patent application FI20031201, filed on 26 August 2003, which is still confidential at the time of filing of the present application.

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Thus, instead of attachment methods forming an electrical contact, it is also possible to use methods, in which an electrical contact is not formed. Such a joint can be made, for example, by gluing the component 6 to the surface of the conductive layer 4 with the aid

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of an insulating adhesive. After gluing, the installation recess 2 can be filled with a filler 8 and the feed-throughs made, through which electrical contacts can be formed between the contact areas of the components 6 and the conductive layer 4. Holes 17 for the feed-throughs are made in the conductive layer 4, at the contact areas of the components 6.

5 The holes 17 are made in such a way that they also break through the adhesive layer that has remained on top of the contact areas, or the contact protrusions. The holes 17 thus extend as far as the material of the contact protrusions or other contact areas of the components 6. The holes 17 can be made, for example, by drilling with a laser device, or by using some other suitable method. After this, conductive material is brought to the
10 holes 17, in such a way that an electrical contact is formed between the component 6 and the conductive layer 4.

After this, the conductive layer 4 can be patterned, so that conductive patterns 14 are formed, at least some of which are connected to some of the contact areas of the
15 components 6. After this, the process can be continued by manufacturing additional conductive-pattern layers and manufacturing the necessary feed-throughs.

Manufacturing processes exploiting an insulating adhesive are disclosed in greater detail in the same applicant's Finnish patent application FI20030493, filed on 1 April 2003,
20 which is still confidential at the time of filing of the present application.

The manufacturing processes according to the examples can be implemented using manufacturing methods that are generally known to one versed in the art of manufacturing circuit boards.

25

In the following, the stages of the method shown in Figures 1 - 17 are examined in greater detail.

Stage A (Figure 1):

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In stage A, a sheet of suitable insulating material 1, from which the body of the installation base is formed, is selected for the electronic-module manufacturing process. In the embodiment using single insulating-material layer, the insulating-material layer 1

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should preferably be thicker than the component being installed. It will then be possible to embed the component entirely inside the installation base and the electronic module will be smooth on both sides. Thicker special components, the rear surface of which will protrude beyond the insulating-material layer 1, can, of course, also be embedded in the installation base. This is the preferred procedure particularly in such embodiments, in which several insulating-material layers are used, which are joined together during the process. In that case, the components can be embedded entirely in the structure, if the total thickness of the insulating-material layer exceeds the thickness of the component. On account of the durability of the structure, it is preferable for the components in the finished electronic module to be located entirely inside the installation base.

The insulating-material layer 1 can be, for example, a polymer base, such as a sheet of glass-fibre reinforced epoxy FR4. Other materials suitable for the insulating-material layer 1 are PI (polyimide), FR5, aramid, polytetrafluoroethylene, Teflon®, LCP (liquid crystal polymer), and a pre-hardened binder layer, i.e. prepreg.

Prepreg refers to one of the basic materials of the circuit-board industry, which is generally a glass-fibre-reinforced insulating mat saturated with B-stage resin. A pre-hardened binder layer is typically used as a binding insulating material, when manufacturing multi-layer circuit-boards. Its B-stage resin is cross-bridged in a controlled manner with the aid of temperature and pressure, for example, by pressing or laminating, so that the resin hardens and becomes C-stage. In the controlled hardening cycle, during the rise in temperature, the resin softens and its viscosity diminishes. Forced by the pressure, the fluid resin fills the holes and openings in its boundary surface. When using a pre-hardened binder layer, this property is exploited to fill the empty space remaining around the components. In this way, it is possible to further simplify the electronic-module manufacturing methods described in the examples, as the installation recesses for the components need not be filled with a separate filler.

The insulating-material layer 1 is surfaced on both sides 1a, 1b, with a conductive layer 4, for example, a metal layer. The manufacturer of the electronic module can also select a ready surfaced insulating sheet as the basic material.

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Stage B (Figure 2):

In stage B, conductive patterns 14 are formed from the conductive layer 4, using some suitable method. The removal of the conductive material can be performed, for example, using vaporization with a laser, or some selective etching method, which are widely used and well known in the circuit-board industry. The conductive pattern 14 is made in such a way that the surface of the insulating-material layer 1 is exposed at the installation recesses 2 made for the components 6, or the side of the surface 1a or 1b.

Correspondingly, the conductive material layer 14 on the opposite surface 1a or 1b of the insulating-material layer 1 is left intact.

Stage C (Figure 3):

In stage C, suitably sized and shaped recesses 2 are made in the insulating-material layer 1, for the embedding of the components. The recesses 2 can be made as required, using, for example, some known method used in circuit-board manufacture. The recesses 2 can be made, for example, using CO₂. The recesses 2 are made from the direction of the second surfaces 1b and extend through the entire insulating-material layer 1, as far as the surface 1a of the conductive material layer 14 on the opposite surface of the layer.

Stage D (Figure 4):

In stage D, the blank of the electronic module is turned the other way round.

Stage E (Figure 5):

In stage E, additional installation recesses 2 in the insulating-material layer 1 are made for components in the direction of the first surface 1a. Otherwise, the recesses 2 can be made in the same way as in stage C.

Stage F (Figure 6):

In stage F, an adhesive layer 5 is spread on the bottom of the installation recesses 2, on

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top of the conductive layer 14. The thickness of the adhesive layer 5 is selected so that the adhesive suitably fills the space between the component 6 and the conductive layer 14, when the component 6 is later pressed onto the adhesive layer 5. If the component 6 includes contact protrusions 7, it would be good for the thickness of the adhesive layer 5 to be greater, for example about 1.5 - 10 times, the height of the contact protrusions, so that the space between the component 6 and the conductive layer 4 will be well filled. The surface area of the adhesive layer 5 formed for the component 6 can also be slightly larger than the corresponding surface area of the component 6, which will also help to avoid the risk of inadequate filling.

10

Stage F can be modified in such a way that the adhesive layer 5 is spread on the attachment surfaces of the components 6, instead of on the attachment areas of the conductive layer 14. This can be carried out, for example, by dipping the component in adhesive, prior to setting it in place in electronic module. It is also possible to proceed by spreading the adhesive on both the attachment areas of the conductive layer 14 and on the attachment surfaces of the components 6.

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The adhesive used in this example is thus an electrical insulator, so that the adhesive layer 5 itself does not form electrical contacts between the contact areas of the component 6.

20

Stage G (Figure 7):

In stage G, the components 6 to be installed from the direction of the first surface 1a are set in place in the electronic module. This can be done, for example, by pressing the components 6 into the adhesive layer 5, with the aid of an assembly machine.

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Stage H (Figure 8):

In stage H, the blank of the electronic module is turned the other way round (see stage D).

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Stage I (Figure 8):

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In stage I, an adhesive layer 5 is spread on the bottom of the installation recesses 2 opening onto the second surface 1b. Stage I is performed correspondingly to stage F, but from the direction of the opposite surface of the electronic module.

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The work stages (e.g., stage F and I) made from opposite sides of the electronic module can, in principle, also be performed simultaneously or consecutively without turning the blank, if the manufacturing equipment being used permits the work stages to be made from two directions.

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Stage J (Figure 9):

In stage J, the components 6 to be installed from the direction of the second surface 1b are set in place, correspondingly to stage G of the electronic module.

15

Stage K (Figure 10):

In stage K, the space remaining between the components 6 and the installation base is filled entirely with a filler 8, which is, for example, some suitable polymer. If the insulating material 1 is a pre-hardened binder layer (prepreg), this stage can be omitted.

20

Stage L (Figure 11):

In stage L, holes 17 are manufactured for the electrical contacts of the components 6. The holes 17 are made through the conductive layer 14 and the adhesive layer 5, in such a way that the material of the contact protrusions, or the corresponding contact areas of the components 6 is exposed. The holes 17 can be made, for example, by drilling with the aid of a laser. A sufficient number of holes 17 is made at the contact areas of the components 6. If, in the process, it is intended to form direct contacts to the components 6 not only through the conductive layer 14, but also through some other conductive layer, the holes 17 need not necessarily be made at contact area participating in such a contact. Typically, in order to form a reliable contact between the contact areas of a component 6 and, for example, a conductive layer 24, a hole 28 is made in two parts; first a hole 17 is

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made between the component 6 and the conductive layer 14 and then a hole 27 is made directly on top of this.

Stage M (Figure 12):

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In stage M, holes 11 are made in the module for the feed-throughs. The holes 11 can be made, for example, mechanically by drilling.

Stage N (Figure 13):

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In stage N, conductive material 15 is grown into the holes 17 made in stage L and into the through-holes 11 made in stage M. In the example process, the conductive material 15 is also grown elsewhere on top of the base, so that the thickness of the conductive layers 14 also increases.

15

The conductive material 15 being grown can be, for example, copper, or some other sufficiently electrically conductive material. In the selection of the conductive material 15, attention must be paid to the ability of the material to form an electrical contact with the material of the contact protrusions 7 or other contact areas of the component 6. In one example process, the conductive material 15 is mainly copper. Copper metallization can be made by surfacing the holes 11 and 17 with a thin layer of chemical copper and after this the surfacing can be continued using an electrochemical copper-growing method. Chemical copper is used, for example, because it will also form a surface on top of adhesive and will act as an electrical conductor in electrochemical surfacing. The growth of the metal can thus be performed using a wet-chemical method, so that the growth is cheap.

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Stage N is intended to form an electrical contact between the components 6 and the conductive layer 14. In stage N, it is not essential to increase the thickness of the conductive layer 14, instead the process can be equally well designed in such a way that, in stage I, the holes 17 and 11 are only filled with a suitable material. The conductive layer 15 can be manufactured, for example, by filling the holes 17 and 11 with an electrically conductive paste, or by using some other metallization method suitable for

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micro-feed-throughs.

In the later figures, the conductive layer 15 is shown merged with the conductive layer 14.

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Stage O (Figure 14):

In stage O, the conductive layers 14 are patterned in such a way that conductive patterns 14 are formed on both surfaces of the sheet 1. The patterning can be performed, for example, in the manner described in stage B.

10

After stage O, the electronic module contains a component 6 or several components 6, as well as conductive patterns 14, with the aid of which the component or components 6 can be connected to an external circuit or to each other. The preconditions then exist for manufacturing an operational totality. The process can thus be designed in such a way that the electronic module is ready after stage O and figure 14 indeed shows one possible electronic module. If desired, the process can also be continued after stage O, for example, by covering the electronic module with a protective substance, or by manufacturing additional conductive-pattern layers on the first and/or second surface:

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Stage P (Figure 15):

In stage P, an insulating-material layer 21 is made on both surfaces of the sheet 1 as well as a conductive layer 24 on top of the insulating-material layer 21. Stage P can be performed, for example, by pressing suitable RCF foils onto both surfaces of the sheet 1. The RCF foil then includes both an insulating-material layer 21 and a conductive layer 24. When the RCF foils are pressed onto the sheet 1 with the aid of heat and pressure, the polymer of the layer 21 forms a unified and tight insulating-material layer between the conductive layer 14 and 24. By means of this procedure, the conductive layer 24 too becomes quite flat and smooth.

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Stage Q (Figure 16):

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In stage Q, holes 27 are made for forming feed-throughs between the conductive layer 14 and 24. The holes can be made, for example, as in stage L using a laser. In some embodiments, it is also possible to make holes 28, with the aid of which a straight feed-through can be formed with the conductive layer 24 and the contact protrusion or contact area of the component 6.

Stage R (Figure 17):

In stage R, conductive material 15 is grown in the holes 27 (and the holes 28) while at the same time the thickness of the conductive layer 24 can also be increased. Stage R can be performed correspondingly to Stage N.

After stage R, the process can be continued by patterning the conductive layers 24 and possibly by manufacturing additional conductive layers on either or both of the surfaces. Separate components can also be connected to the conductive layer on the surface of the electronic module, in the conventional manner of circuit-board technology.

The following will deal, with the aid of Figures 18 - 27, with some possible modifications of the manufacturing process.

Stage A2 (Figure 18):

In stage A2, as in stage A, a suitable insulating-material sheet 1, from which the body of the installation base is formed, is selected for the manufacturing process of the electronic module. In the example process, the insulating-material layer 1 is surfaced from the first surface 1a with a conductive layer 4, for example, a metal layer.

Stage B2 (Figure 19):

In stage B2, as in stage C, suitably sized and shaped recesses 2 are manufactured in the insulating-material layer 1, for the components to be embedded in the sheet. The recesses 2 are made from the direction of the second surface 1b and extend through the entire insulating-material layer 1 as far as the surface of the conductive-material layer 4 on the

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opposite surface of the layer.

Stage C2 (Figure 20):

- 5 In stage C2, the components 6 to be installed from the direction of the second surface 1b are set in place in the recesses 2 and connected to the conductive layer 4. Electrical contacts are then also formed between the contact protrusions or contact areas of the components and the conductive layer 4. The connection of the components 6 can be made, for example, by gluing with an isotropically or anisotropically electrically
- 10 conductive adhesive. The attachment can also be performed using some other applicable method, for example, the ultrasonic or thermo-compression method.

Stage D2 (Figure 20):

- 15 In stage D2, the space remaining between the component 6 and the installation base is entirely filled with a filler 8, which is, for example, some suitable polymer.

Stage E2, (Figure 21):

- 20 In stage E2, conductive patterns 14 are formed from the conductive layer 4, using some suitable method. The removal of the conductive material can be performed, for example, by vaporization with a laser, or using one of the selective etching methods, which are widely using and well known in the circuit-board industry.

25 **Stage F2 (Figure 22):**

In stage F2, a conductive layer 9 is formed on the second surface 1b of the sheet 1. Stage F2 can be performed, for example, by laminating an RCF-foil onto the second surface 1b.

30 **Stage G2 (Figure 23):**

In stage G2, recesses 2 for components are made in the insulating-material layer 1, as in stage B2. Now the recesses 2 are manufactured from the direction of the first surface 1a

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and extend as far as the surface of the conductive-material layer 9.

Stage H2 (Figure 24):

- 5 In stage H2, the components 6 to be installed from the direction of the first surface 1a are connected to the conductive layer 9. The stage can be performed as in stage C2.

Stage I2 (Figure 20):

- 10 In stage I2, the space remaining between the components 6 and the installation base is entirely filled with a filler 8, which is, for example, some suitable polymer.

Stage J2 (Figure 25):

- 15 In stage J2, conductive patterns 19 are formed from the conductive layer 9, using a suitable method.

Stage K2 (Figure 26):

- 20 In stage K2, holes 27 are made to form feed-throughs between the conductive-pattern layers 14 and 19.

Stage L2 (Figure 27):

- 25 In stage L2, conductive material is grown in the holes 27. Stage L2 can be performed correspondingly as stage N.

After stage L2, the electronic module includes two conductive-pattern layers and embedded components 6 connected to them. If several conductive layers are not required
30 in the electronic module being manufactured, the module can, for example, be protected with a protective substance after stage L2. After stage L2, it is also possible, if wished, to manufacture additional conductive layers in the module, or to connect surface-assembled components to it. The modules can also be connected to each other to create a multi-layer

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structure.

The above descriptions are of embodiments, in which the insulating-material layer 1 is formed from a single unified insulating-material sheet, for example, a glass-fibre-reinforced epoxy sheet, or a prepreg sheet. However, the insulating-material layer 1 can equally well be manufactured from more than one part. It is then also possible to proceed in such a way that the insulating-material layer 1 is formed of more than one insulating material. Figures 28 - 31 shows two such embodiments.

Figure 28 shows an element, which includes a first insulating-material layer 1, with components 6 set in recesses made in it. In addition, the element includes a conductive layer 4 on the surface of the insulating-material layer 1. The thickness of the first insulating-material layer 1 is preferably greater than the height of the components 6. An element like that shown in the figure can be manufactured, for example, by combining the sub-processes of the previous series of figures. In addition to the element, the figures also show a second insulating-material layer 11, in which recesses 22 for components 6, as well as a second conductive layer 9 are also made.

In the above embodiment, the second insulating-material layer 11 is prepreg. The first insulating-material layer 1 can then also be of some other insulating material, for example, a glass-fibre-reinforced epoxy sheet. After this, the layer are joined together, resulting in the element depicted in figure 29. As can be seen from figure 28, the resin contained in the prepreg fills the space between the component 6 and its surroundings. After this, the manufacture of the electronic module can be continued with the aid of the sub-processes described above.

Figure 30 shows a first and a second element, both of which include a first insulating-material layer 1 and components 6 in recesses manufactured in the insulating-material layer 1. In addition, the elements include conductive layers 4 on the first surfaces of the insulating-material layers 1 and conductive patterns 19 on the second surfaces of the insulating-material layers 1. In both elements, the thickness of the first insulating-material layer is greater than the height of the components 6. The second element is rotated relative to the first element in such a way that the conductive patterns 19 face

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each other and a second insulating-material layer 11, in which recesses 22 are also made for components 6, is placed between the elements. After this, the elements are the second insulating-material layer 11 are attached to each other, resulting in the module structure shown in figure 31. The structure of figure 31 is compact and thin, and, at this stage of the process, it already includes four conductive layers (layers 4, 4, 19, and 19).

In the embodiment shown in figures 30 and 31 too, prepreg can be used as the second insulating-material layer 11. The first insulating-material layer 1 of both elements can then also be some other insulating material, for example, a glass-fibre-reinforced epoxy sheet. With the aid of the prepreg, excellent filling is achieved in the space between the elements, as shown in the example of figure 29. The modules shown in figures 30 and 31 can be made, for example, with the aid of the sub-processes described above. The manufacture of the electronic module can also be continued in a corresponding manner as, for example, that of the elements shown in figure 10 or figure 24, making allowance for the selected connection techniques, the patterning requirement of the conductive layers 4, and other similar special requirements of the process.

The examples of the previous figure series shown some possible processes, with the aid of which our invention can be exploited. Our invention is not, however, restricted to only the processes disclosed above, but instead the invention also covers other different processes and their end products, taking into account the full scope and equivalence interpretation of the Claims. The invention is also not restricted to the constructions and methods described in the examples, it being obvious to one versed in the art that various applications of our invention can be used to manufacture very many different electronic modules and circuit boards, which differ from the examples described above to even a great extent. The components and connections of the figures are thus presented only to illustrate the manufacturing process. Thus, a great many alterations can be made to the process of the examples given above, without, however, deviating from the basic idea according to the invention. The alterations can relate, for example, to the manufacturing techniques described in the different stages, or to the mutual sequence of the process stages.

In the processes described above, it is possible, for example, to use several techniques for

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attaching the components, for example, in such a way that the components to be attached from the direction of the first surface are attached using some first technique and the components to be attached from the direction of the second surface are attached using some second technique, which differs from the said first technique.

5

In the examples given above, electronic modules are manufactured, which include components embedded from a first and a second direction. Of course, within the scope of the invention, it is also possible to manufacture such simpler modules, which include only components embedded from one direction. With the aid of such simpler modules, it is also possible a module including components embedded in two directions. The module can be manufactured, for example, in such a way that two modules are laminated together from their 'back' sides, so that the active surfaces contained in the sub-modules face the opposite outward surface of the module that has been laminated together.

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Claims:

1. A method for manufacturing an electronic module, characterized by:

5 - taking a sheet, which has a first (1a) and a second (1b) surface, and which sheet includes an insulating-material layer (1) between the first (1a) and the second (1b) surface, as well as a conductive layer (4) on at least the first surface (1a),

10 - making at least one recess (2) in the sheet (1) that extends through the second surface (1b) and the insulating-material layer (1) as far as the conductive layer (4) on the first surface (1a), which covers the recess (2) from the direction of the first surface (1a),

15 - taking a component (6) having a contact surface with contact areas or contact protrusions,

20 - placing the component (6) in the recess (2) with its contact surface facing the first surface (1a) and attaching the component (6) to the conductive layer (4), which covers the recess (2) from the direction of the first surface (1a), and

25 - forming a conductive pattern (14), which connects electrically to at least some of the contact areas or contact protrusions of the component (6) placed in the recess (2), from the conductive layer (4) covering the recess (2).

30 2. A method according to Claim 1, wherein the components (6) are placed facing both the first (1a) and second (1b) surface in the insulating-material layer (1) and electrical contacts are formed to the components (6) in such a way that at least some of the components are connected to the conductive layer (4) on the first surface (1a) and at least some to the conductive layer (4) on the second surface (1b).

3. A method according to Claim 1 or 2, comprising performing, after the component (6) or several components (6) have been attached to the conductive layer (4), which closes the recess (2) or recesses (2) from the direction of the first surface (1a), the following

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steps:

- making a conductive layer (9) on the second surface (1b) of the sheet,
 - 5 - making at least one recess (2) in the sheet (1), which extends through the first surface (1a) and the insulating-material layer (1) as far as the conductive layer (9) on the second surface (1b), which covers the recess (2) from the direction of the second surface (1b),
 - 10 - taking a component (6) having a contact surface with contact areas or contact protrusions,
 - placing the component (6) in the recess (2), with its contact surface towards the second surface (1b) and attaching the component (6) to the conductive layer (4),
 - 15 which covers the recess (2) from the direction of the second surface (1b), and
 - forming a conductive pattern (19) from the conductive layer (9) covering the recess (2), which pattern is connected electrically to at least some of the contact areas or contact protrusions of the component (6) placed in the recess (2).
 - 20
4. A method according to Claim 1 or 2, wherein a sheet is used, which is surfaced with a conductive layer (4) on both surfaces, and in which
- 25 - at least one second recess (2) is manufactured in the sheet (1), and extends through the first surface (1a) and the insulating-material layer (1) as far as the conductive layer (4) on the second surface (1b), which covers the manufactured recess (2) from the direction of the second surface (1b),
 - a component (6) is taken, which has a contact surface with contact areas or
 - 30 contact protrusions,
 - the component (6) is placed in the recess (2) with its contact surface facing the second surface (1b) and the component (6) is attached to the conductive layer (4),

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which covers the recess (2) from the direction of the second surface (1b), and

- a conductive pattern (14) is formed from the conductive layer (4) covering the recess (2), which pattern is electrically connected to at least some of the contact areas or contact protrusions of the component (6) set in the recess (2).

5. A method according to Claim 1 or 2, wherein the thickness of the insulating-material layer (1) is less than the thickness of at least one component (6) attached to the conductive layer and in which:

- a least one second insulating-material sheet (11) is taken,

- at least one recess (2) for the said at least one component (6) attached to the conductive layer (4) is made in the second insulating-material sheet (11), and

- the second insulating-material sheet (11) is attached to the first insulating-material layer (1) from the direction of the second surface (1b).

6. A method according to Claim 1, 2, or 5, wherein a first and a second element are manufactured, both of which include an insulating-material layer (1), a conductive layer (4) on at least the first surface (1a) of the insulating-material layer (1), and at least one component (6) in at least one recess (2), and in which method:

- at least one second insulating-material sheet (11) is taken, and

- the first and the second elements are attached to each other with the aid of the said second insulating-material sheet (11), in such a way that the second surfaces (1b) of the insulating-material layers (1) contained in the elements face towards each other.

7. A method according to Claims 5 or 6, wherein the first insulating-material layer (1) is of a first insulating material and the second insulating-material sheet (11) is of a second insulating material, which differs from the first insulating material.

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8. A method according to any of Claims 1 - 7, wherein at least one component (6) is attached to the conductive layer (4; 9) by gluing with the aid of an electrically conductive adhesive, so that an electrical contact is formed between the conductive layer (4; 9) and the contact areas or contact protrusions of the component (6).

9. A method according to any of Claims 1 - 7, wherein at least one component (6) is attached to the conductive layer (4; 9) by gluing with the aid of an electrically insulating adhesive and an electrical contact is formed between the component (6) and the conductive layer (4; 9) by making feed-throughs, which connect the desired contact areas to the conductive layer (4; 9).

10. A method according to any of Claims 1 - 7, wherein at least one component (6) is attached, and electrical contact with the conductive layer (4; 9) is formed by bonding the contact areas metallurgically to the conductive layer (4; 9), either directly, or through intermediary contact protrusions.

11. A method according to any of Claims 1 - 10, wherein at least one component (6) attached to the conductive layer (4; 9) is an unpacked microcircuit chip.

12. A method according to any of Claims 1 - 11, wherein, in order to create a multi-layer circuit-board structure, additional insulating layers and conductive layers are manufactured on the first (1a) and/or the second (1b) surface.

13. A method according to any of Claims 1 - 12, wherein the components (6) are embedded in at least two sheets (1), which are subsequently attached on top of each other.

14. A method according to any of Claims 1 - 13, wherein a conductive-pattern layer (14; 19) is manufactured on both the first (1a) and the second (1b) surfaces of the insulating-material layer (1).

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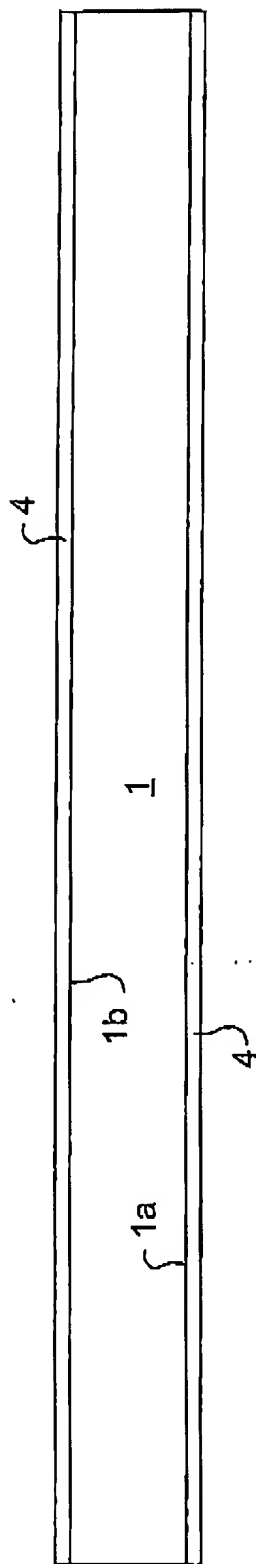


Fig. 1

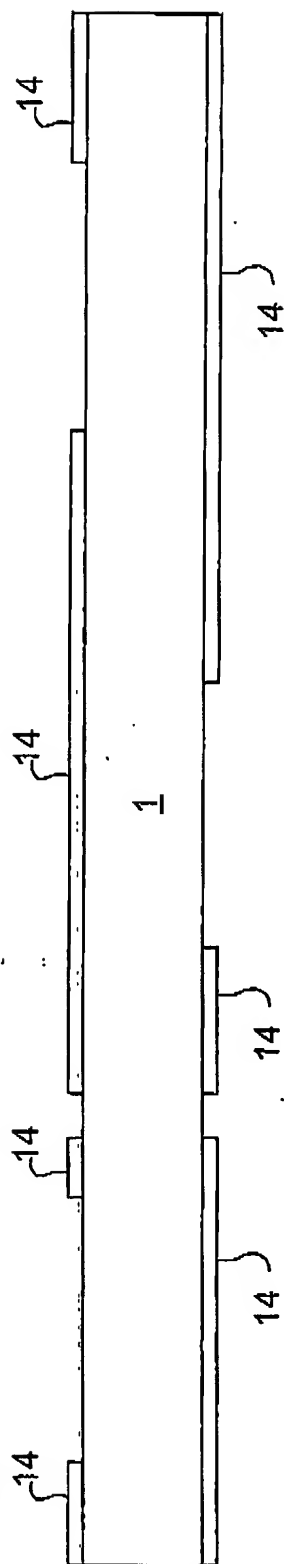


Fig. 2

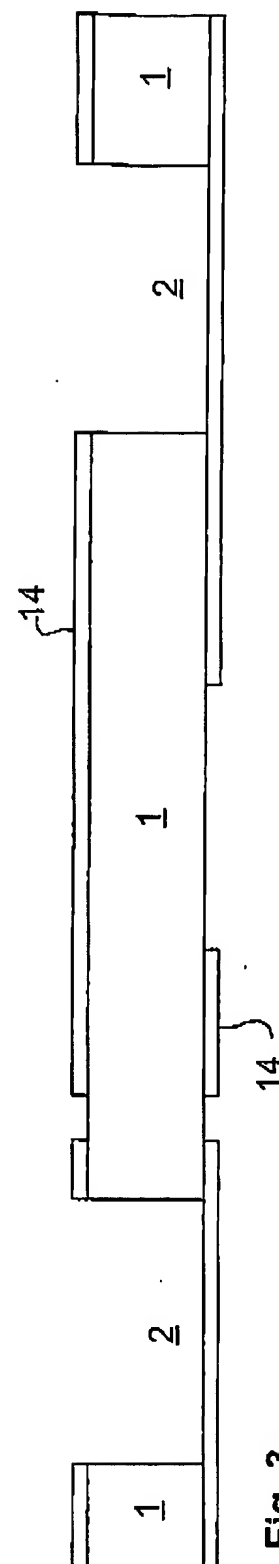


Fig. 3

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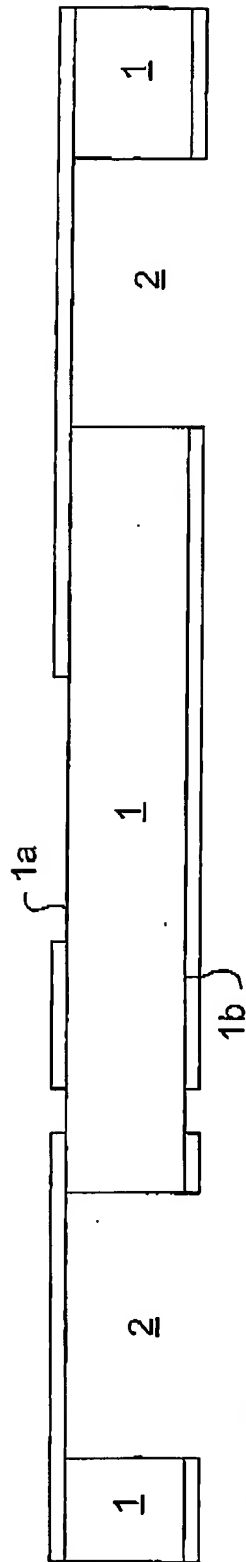


Fig. 4

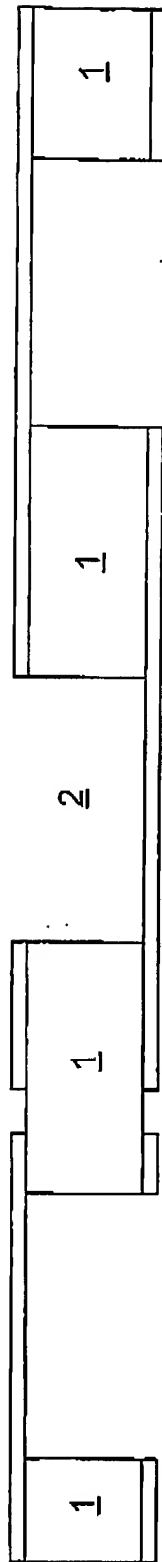


Fig. 5

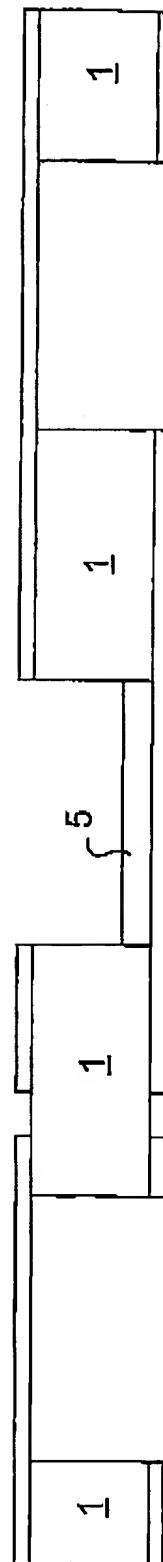


Fig 6

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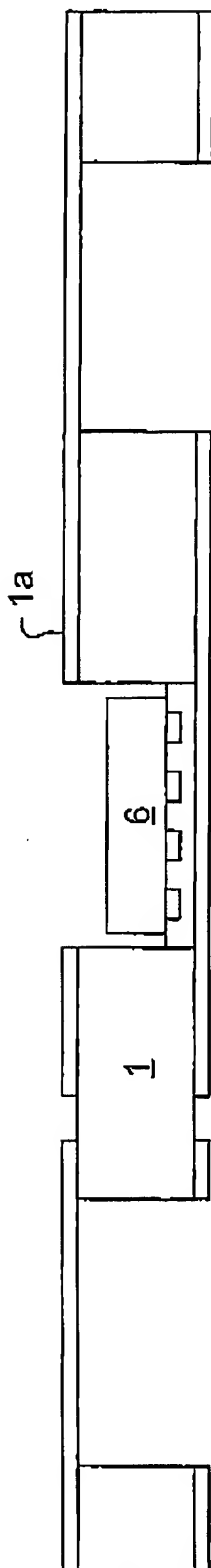


Fig. 7

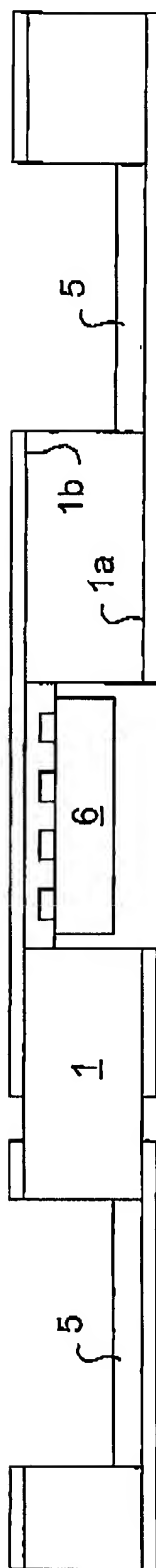


Fig. 8

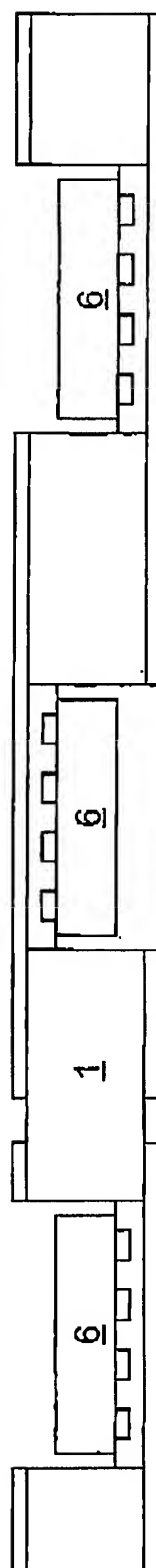


Fig. 9

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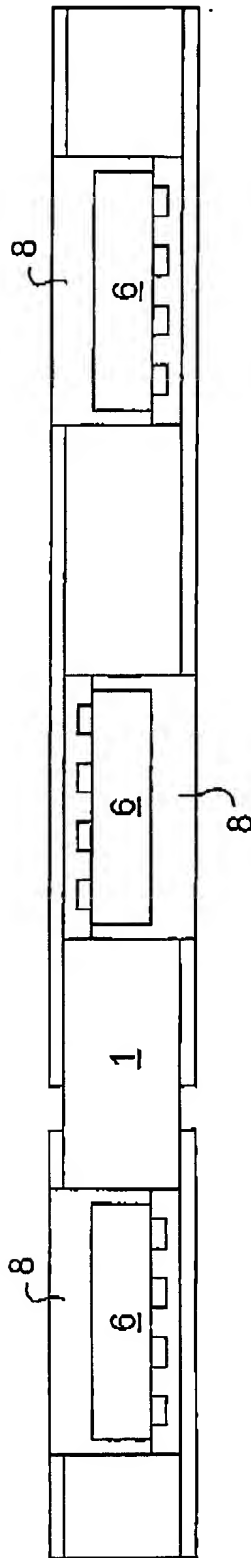


Fig. 10

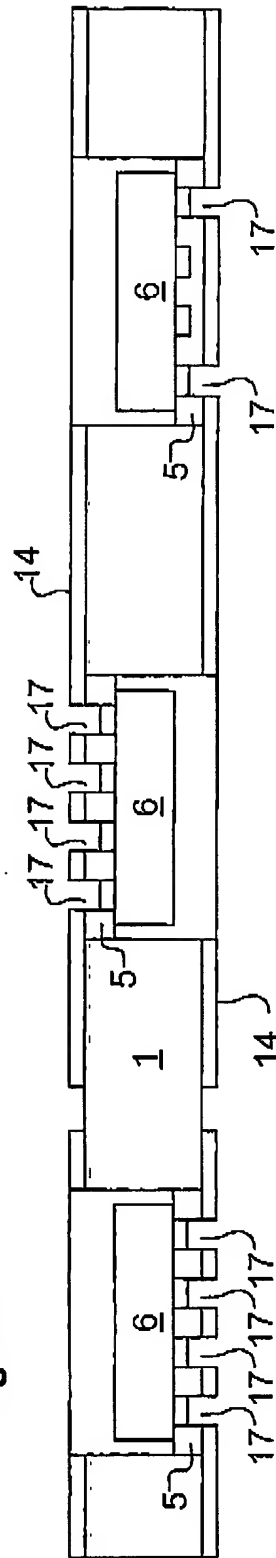


Fig. 11

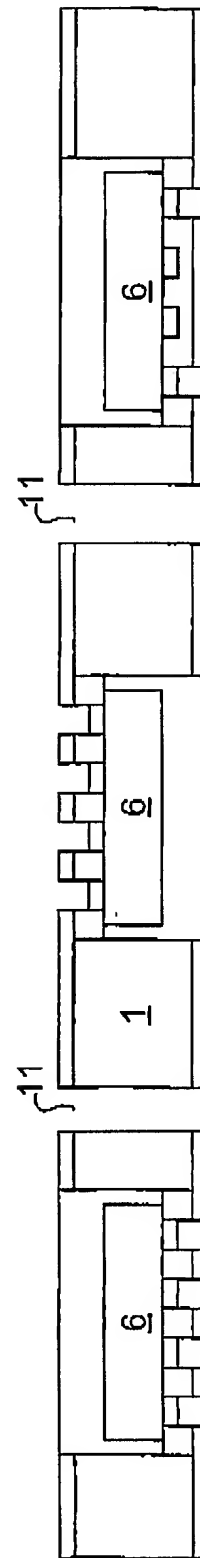


Fig. 12

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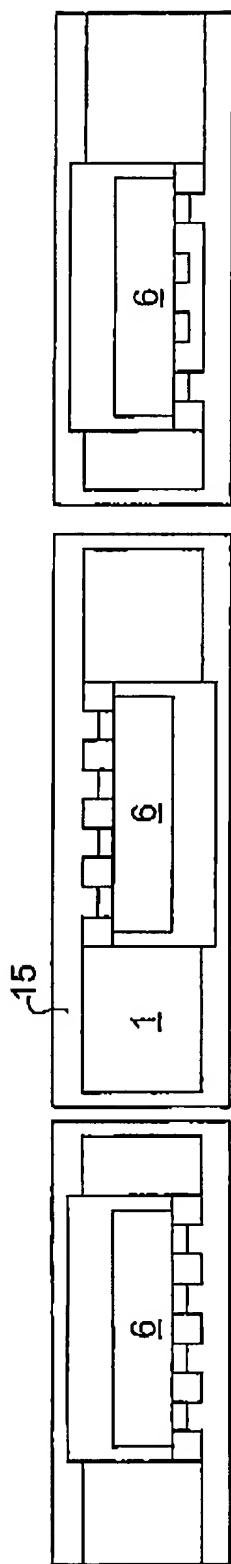


Fig. 13

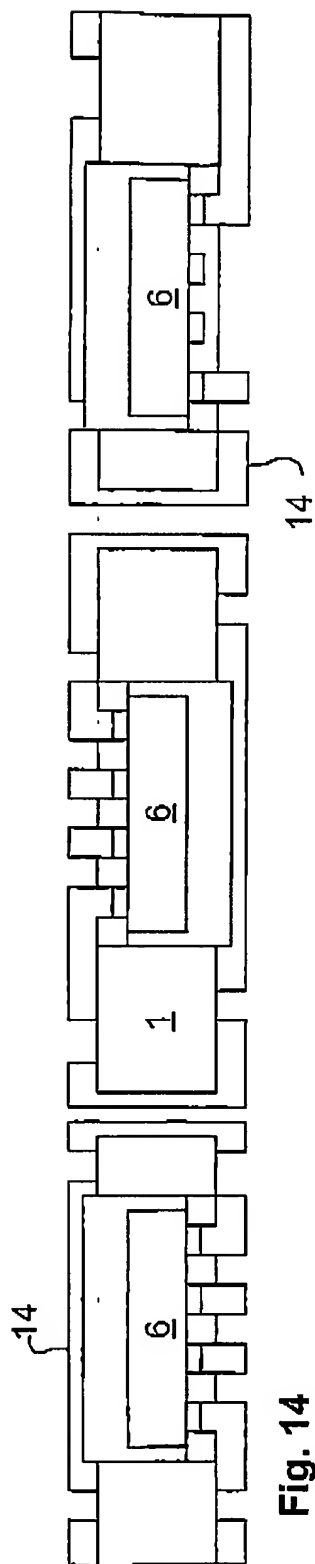


Fig. 14

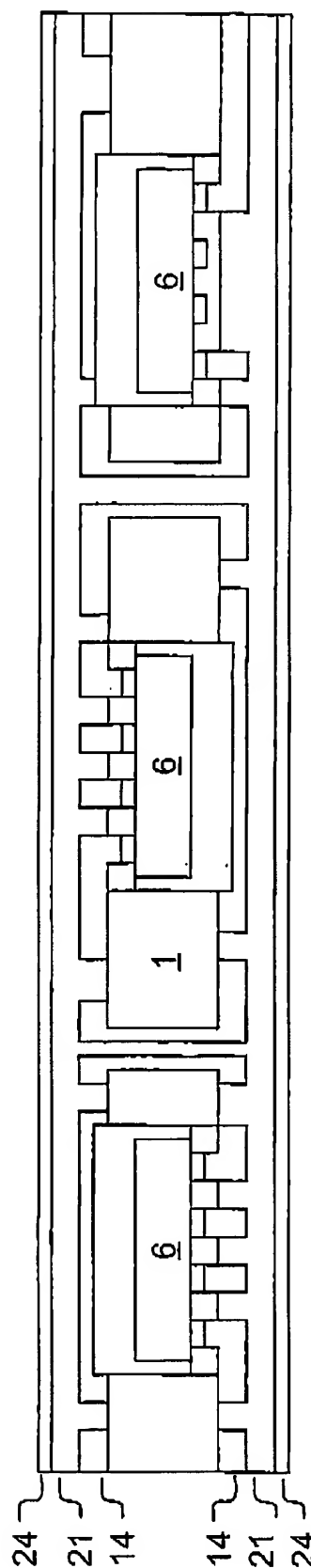


Fig. 15

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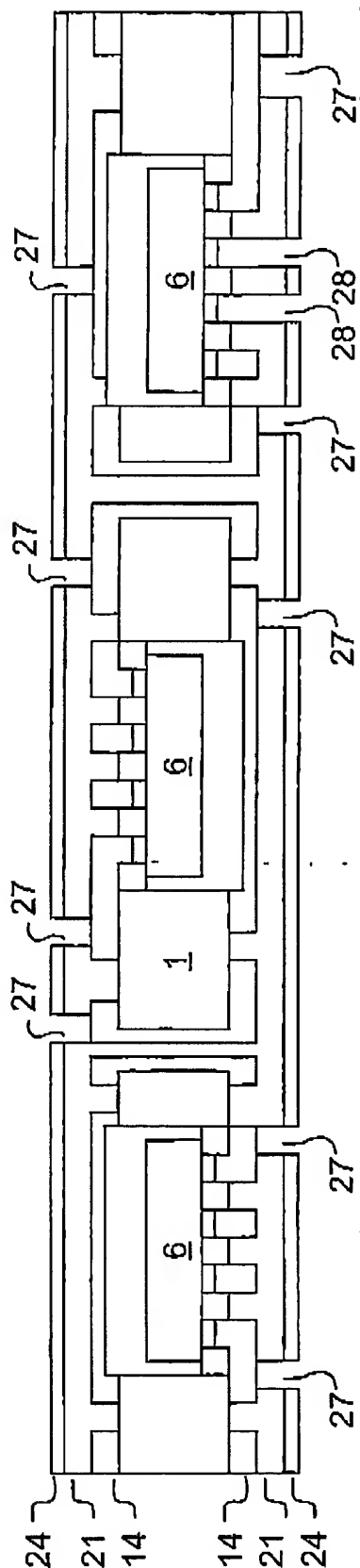


Fig. 16

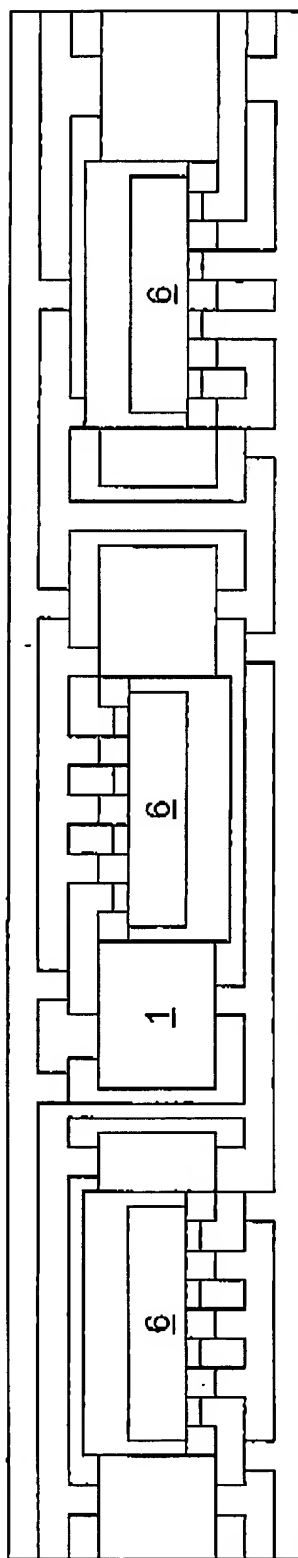
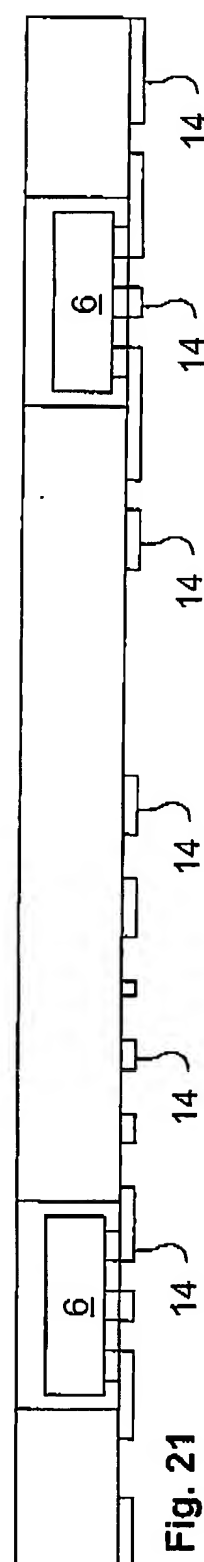
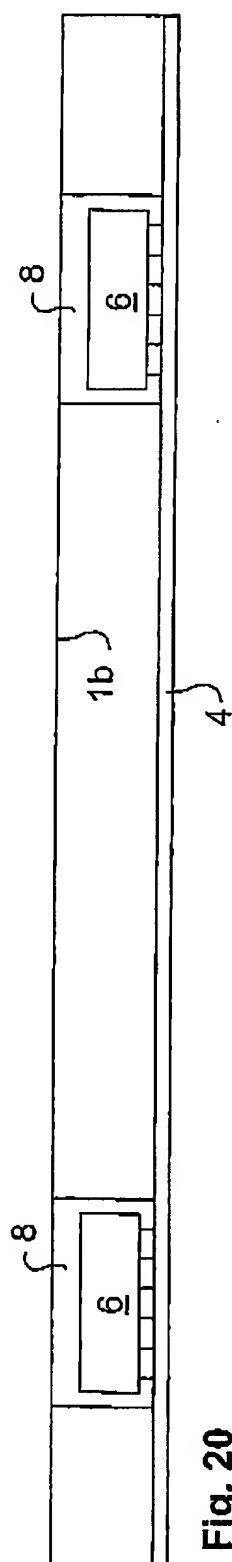
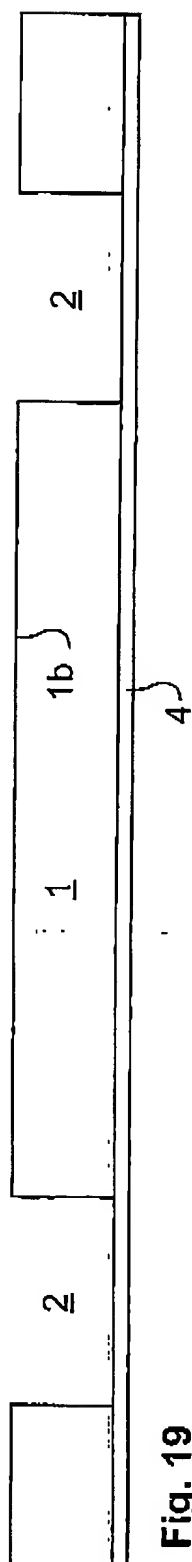
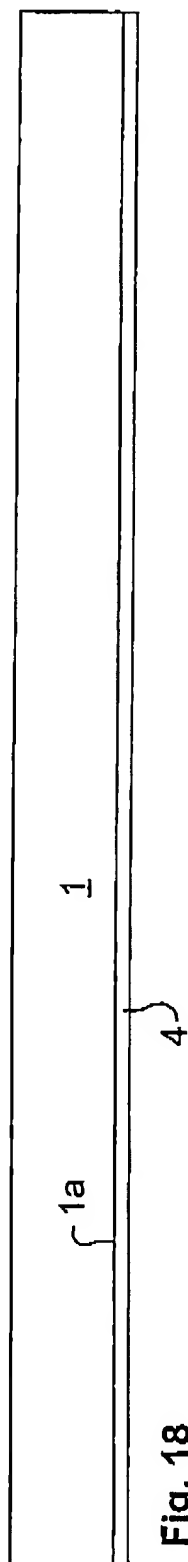


Fig. 17

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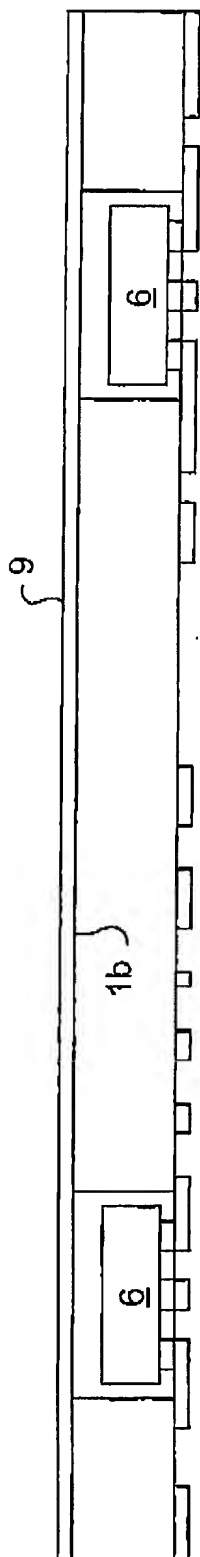


Fig. 22

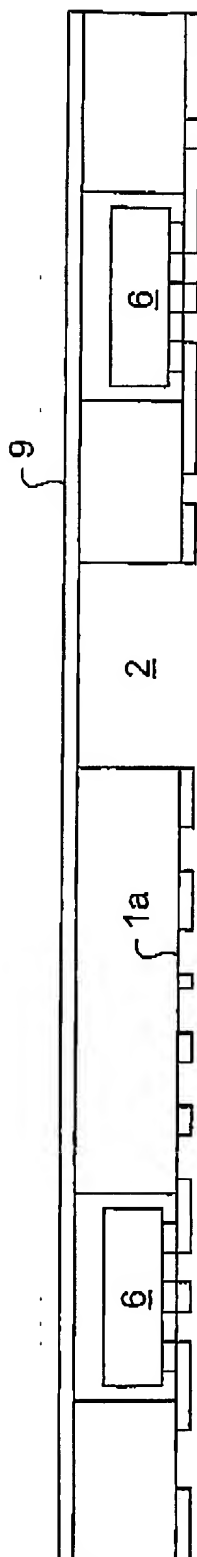


Fig. 23

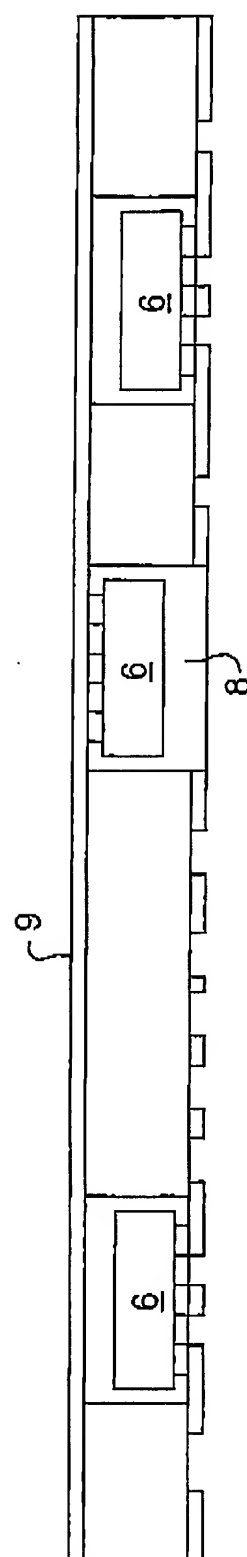


Fig. 24

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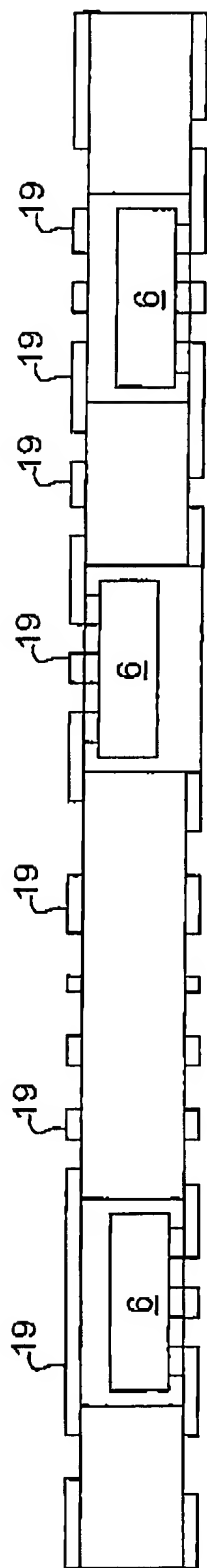


Fig. 25

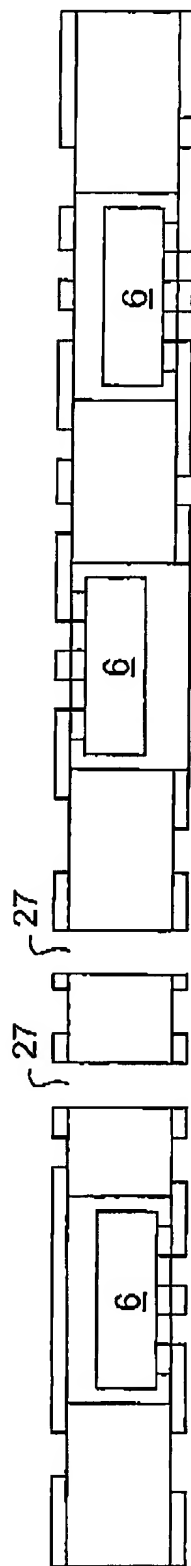


Fig. 26

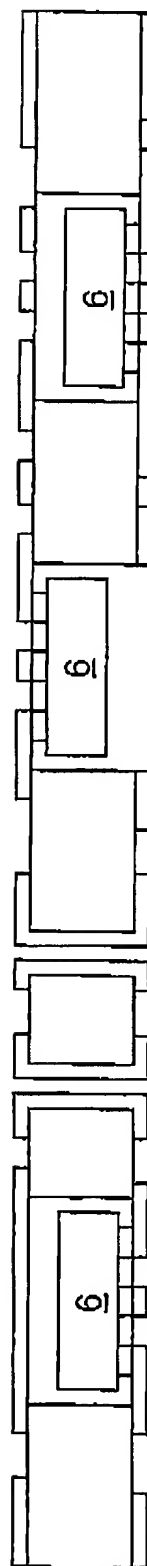


Fig. 27

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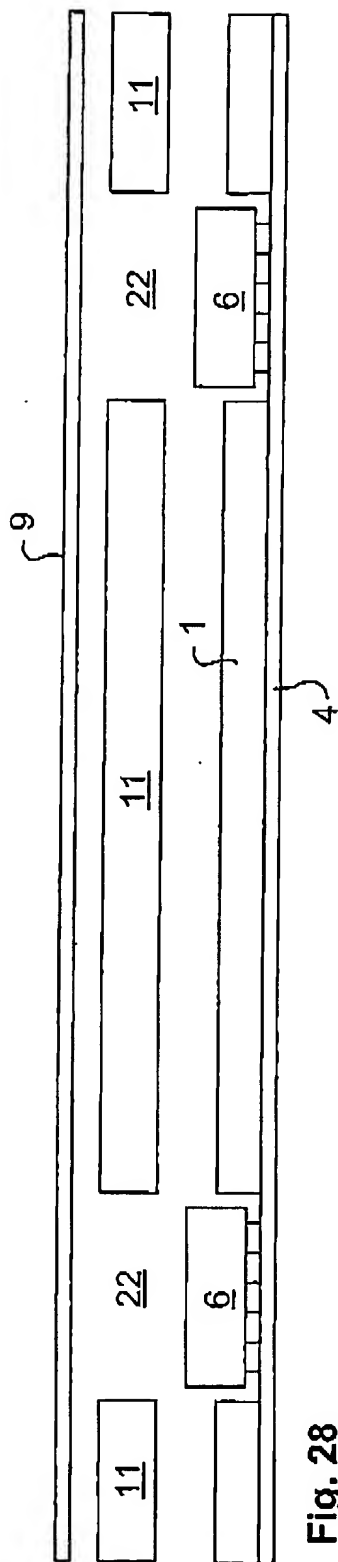


Fig. 28

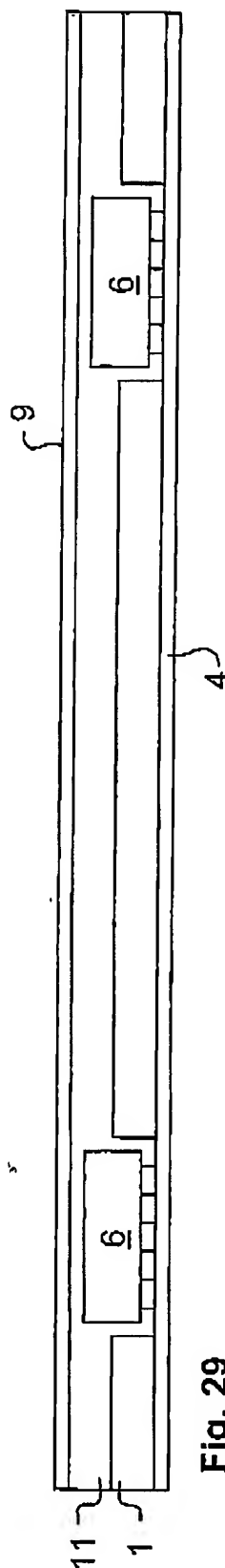


Fig. 29

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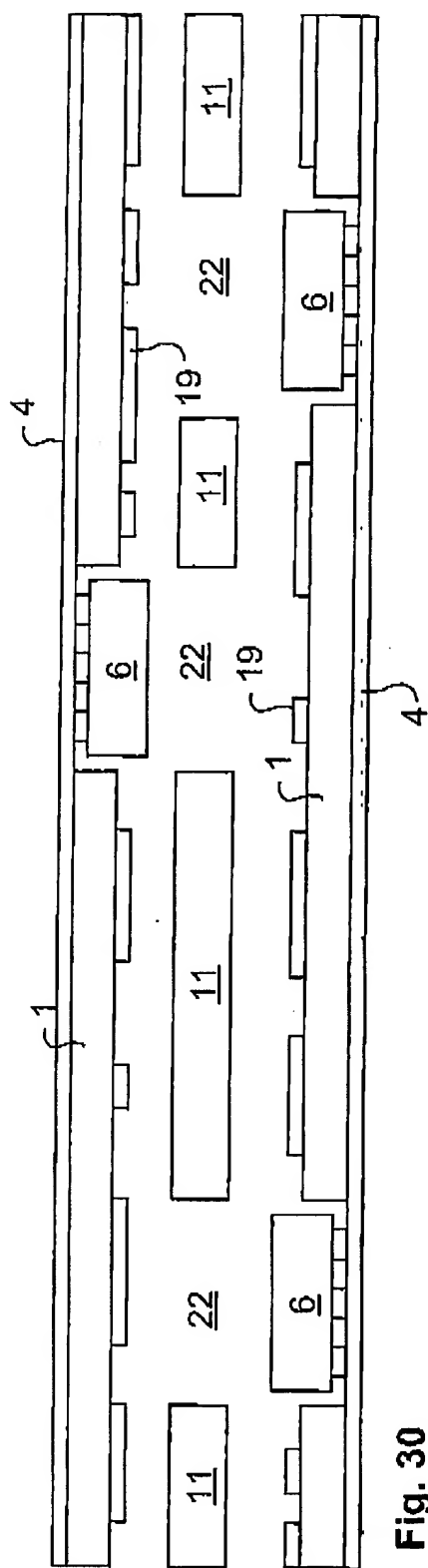


Fig. 30

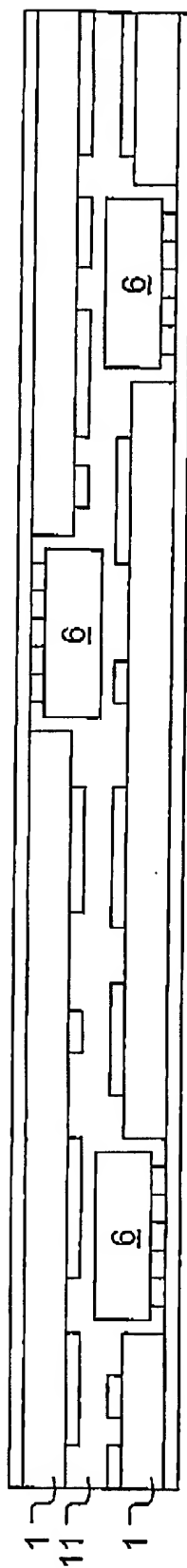


Fig. 31

INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 2004/000538

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H05K 3/32, H05K 3/46

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H05K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-INTERNAL, WPI DATA, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5042145 A (JAN P. BOUCQUET), 27 August 1991 (27.08.1991), column 3, line 50 - column 4, line 35, figures 6-9 --	1,10-11
X	EP 0774888 A2 (MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD), 21 May 1997 (21.05.1997), column 8, line 17 - line 27; column 7, line 29 - line 39, figures 1,8,12-14 --	1,8,12-14
A	EP 1156525 A1 (IBIDEN CO., LTD.), 21 November 2001 (21.11.2001), [0013]-[0030] --	1-14

☒ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

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Date of the actual completion of the international search

31 January 2005

Date of mailing of the international search report

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Box 5055, S-102 42 STOCKHOLM
Facsimile No. +46 8 666 02 86

Authorized officer

Anna Flodman/MN
Telephone No. +46 8 782 25 00

INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 2004/000538

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 20020053465 A1 (YOSHIHIRO KAWAKITA ET AL), 9 May 2002 (09.05.2002), [0004]-[0014] ---	1-14
A	US 4993148 A (KOHEI ADACHI ET AL), 19 February 1991 (19.02.1991), column 3, line 1 - column 5, line 30 -- -----	1-14

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

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US	5042145	A	27/08/1991	AT	93075	T	15/08/1993
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